

ABSTRACT OF THE DISCLOSURE

A source driver 92 of the present invention has a reference voltage generator 38 for generating tone display voltages, and a DA converter 36 for selecting and outputting a tone display voltage to a liquid crystal panel. In the source driver 92, a buffer circuit section 41 is provided between the reference voltage generator 38 and the DA converter 36. The buffer circuit section 41 includes a buffer, and analog switch circuits which switch modes of connection between the reference voltage generator 38, the buffer, and the DA converter 36, so as to select whether to output the tone display voltage to the DA converter 36 via the buffer or without utilizing the buffer. Operations of the analog switch circuits are controlled by the analog switch circuit section 40.